



PTO/SB/08A (08-03)

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**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**

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Complete if Known

Application Number	10/766,801
Filing Date	27 January 2004
First Named Inventor	Ron McCallister
Art Unit	
Examiner Name	
Attorney Docket Number	2298-030

Sheet 1 of 2

U. S. PATENT DOCUMENTS

Examiner Initials*	Cite No. ¹	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind Code ² (if known)			
25		US- 2003/0227981	12/11/2003	Vella-Coleiro, et al	
		US- 2003/0076894	04/24/2003	Jin, et al.	
		US- 2002/0180523	12/05/2002	Okubo, et al.	
		US- 2004/0021517	02/05/2004	Irvine, et al.	
		US- 2004/0013206	01/22/2004	Gamm, et al.	
25		US- 2003/0020538	01/30/2003	Kim	
		US- 5,760,646	06/02/1998	Belcher, et al.	
		US- 6,118,335	09/12/2000	Nielsen, et al.	
		US- 5,923,712	07/13/1999	Leyendecker, et al.	
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FOREIGN PATENT DOCUMENTS

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		Country Code ³ Number ⁴ Kind Code ⁵ (if known)				

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Sheet	2	of	2	Attorney Docket Number	2298-030
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NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
25/5		GLENTIS, KOUKOULAS, & KALOUPSIDIS "Efficient Algorithms for Volterra System Identification" IEEE Transactions on Signal Processing, Vol 47, No. 11, Nov 1999.	
11		DING, RAICH, & ZHOU "A Hammerstein Predistortion Linearization Design Based on the Indirect Learning Architecture" Georgia Inst. of Tech. School of Elect. & Comp. Engineering.	
		RAZZAGHI & CHANG "A 10-b,1GSample/s ADC using SiGe BiCMOS Technology" Electrical Engineering Department, UCLA.	
		DANESHRAD "DSP VLSI Engine for Electronic Linearization of Fiber Optic Links" Wireless Integrated Systems Research Group, UCLA, 02/27/2001.	
11		FOX "Analysis and Dynamic Range Enhancement of the Analog-to-Digital Interface in Multimode Radio Receivers" Virginia Polytechnic Inst. & State Univ. 02/1997.	
25/5		HUMMELS "Linearization of ADCS and DACS for All-Digital Wide-Bandwidth Receivers" Depart. of Electrical & Comp. Engineering, Univ. of Maine, 09/09/1999.	

Examiner Signature		Date Considered	5/12/05
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U. S. PATENT DOCUMENTS

U. S. PATENT DOCUMENTS					
Examiner Initials*	Cite No. ¹	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind Code ² (if known)			
ACB ↑		US- 5,778,029	07/07/1998	Kaufmann	
		US- 6,282,247	08/28/2001	Shen	
		US- 6,222,878	04/24/2001	McCallister, et al.	
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Examiner Signature		Date Considered	5/12/23
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		Number-Kind Code ² (if known)			
E/S		US- 5,113,414	05/12/1992	Karam, et al.	
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		US- 4,910,698	03/20/1990	McCartney	
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U		US- 4,290,139	09/15/1981	Walsh	
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	First Named Inventor		Ronald D McCallister
	Art Unit		
	Examiner Name		
Sheet 3	of 3	Attorney Docket Number 2298-030	

NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
α-β		CAVERS, "The Effect of Quadrature Modulator and Demodulator Errors on Adaptive Digital Predistorters for Amplifier Linearization" IEEE Trans on Veh Tech, Vol46, #2, 05/97.	
↑		DING & ZHOU, "Effects of Even-order Nonlinear Terms on Power Amplifier Modeling and Predistortion Linearization" IEEE Transactions on Vehicular Technology, 08/2002	
		GRASS, BODHISATYA & MAHARATNA, "A Dual-Mode Synchronous/Asynchronous CORDIC Processor" IHP, IM Technologiepark, 2002.	
		OHLSSON, "Studies on Implementation of Digital Filters With High Throughput and Low Power Consumption" Institute of Technology, Dept of Electrical Engineering, P 61-73, 06/03.	
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		GODAVARTI "Multiple Antennas in Wireless Communications" Array Signal Processing and Channel Capacity" University of Michigan, P 16-52, 2001.	
		"Pact Application Note: Adaptive Filter Based on the LMS-Algorithm"	
↓		CHOO "Mapping LMS Adaptive Filter IP Core to Multiplier-Array FPGA Architecture for High Channel-Density VOIP Line Echo Cancellation" CMP United Business Media, 2003.	
α-β		WIDROW, MCCOOL & BALL, "The Complex LMS Algorithm" Information Systems Laboratory, Naval Undersea Center, Fleet Engineering Dept, 08/19/1974.	

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